



Hardware Processing Platform (HPP): Getting Started Guide

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Rev. 2.0 1/17/2020

Hardware Processing Platform (HPP) Getting Started Guide



1 Introduction

The Neuralynx Hardware Processing Platform (HPP) runs code developed on a PC and is downloaded through a USB connection. This "Getting Started Guide" walks you through the process of getting the HPP development environment installed and tested.

2 Installing Xilinx Development Software

The development software used with the HPP is the Xilinx Vivado and SDK. The first step is to download this software. Use this link for the download web page:

https://www.xilinx.com/support/download/index.html/content/xilinx/en/downloadNav/vivado-design-tools/a rchive.html



The version to download is "Vivado Design Suite – 2014.1 Full Product Installation".

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For this guide, we will target a Windows development environment. <u>Vivado 2014.1 Full Image for Windows</u> <u>with SDK</u> (TAR/GZIP - 5.1 GB). A Xilinx user account will be required for download. If you do not already have one, you can sign up for a free Xilinx account using the "Create your Account" link.

After the file has been downloaded, unzip and untar the compressed files. If you don't have an application installed that can unzip and untar a file, you can download the latest version of 7-zip from www.7-zip.org. Once the installation files have been decompressed, run the xsetup.exe executable located in the extracted Xilinx_Vivado_SDK_Win_2014.1_0405_1 folder.

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Xilinx Vivado Installation

If the installation prompts to install a newer version, select "Continue" to proceed with installing the 2014.1 version of Xilinx Design Tools. (Neuralynx plans to release an updated HPP platform to support newer versions of Xilinx Development tools in the future).



Once the installation process has been started, you will be prompted for the edition to install. You should select the "Vivado Design Edition".



Edition to Install



Install with the following options selected:



Vivado Design Edition Install Options

At a minimum, ensure that

- 1. The "Software Development Kit" option is selected under Design Tools,
- 2. "Zynq-7000" option is selected under Devices, and
- 3. "Install Cable Drivers" and "Acquire or Manage a License Key" is selected under Installation Options.



When the installation finishes, the Vivado License Manager will open. Select "Obtain License" under Getting a License.



Vivado License Manager

Select "Get Free Licenses – Vivado WebPack, SDK, free IP and more", then click the "Connect Now" button.

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You will need to sign up for a free Xilinx account using the "Create your account" link to get a license for the Xilinx software if you have not already done so.

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Xilinx Account Sign In Page

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Account information



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Activate Account

Click on the link in your email to activate your account. This will take you to the Xilinx login webpage. Sign in with your newly created account.

Enter the required profile information (indicated by *) and click "Save Profile."

Return to the Vivado license manager. Click on "Obtain License" and select "Get Free Licenses – Vivado WebPack, SDK, free IP and more." Then click "Connect Now." This will take you back to the licensing web page.



From the Product Licensing web page that opens, verify your user information and click "Next."

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Product Licensing Information



Check the box in "Certificate Based Licenses" for "Vivado Design Suite: HL WebPACK 2015 and Earlier License" and click "Generate Node-Locked License."

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Generate License



This page will use a host ID from the current PC to license the development software.

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License Generation Complete



A Xilinx license file will be sent to your provided email address from xilinx.notification. Download the "Xilinx.lic" file to a known location.

Return to the Vivado License Manager. Select on "Load License" under Getting a License and click the "Copy License" button.

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File Help Getting a License Proxy Settings Obtain License Coad License Managing My Licenses License Search Locations View License Status Return License to Xilinx System Settings System Information	Load License Certificate Based Licenses Click the 'Copy License' button to copy a certificate-based license (.lic file) into the local .Xlinx directory. Xlinx applications automatically detect valid, node-locked licenses (*.lic) residing in the local .Xlinx directory. Copy License Activation Based Licenses Click the 'Activate License' button to load a response XML file into VLM to activate your machine for Xlinx tools and IP. Activate License

Load License

Browse to your downloaded "Xilinx.lic" license file and click "Open". Click "Ok" to close the "license installation was successful" message box. Close the Vivado License Manager and complete the Vivado installation.

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3 Programming a Project on the HPP

Download the provided base example project for the HPP from: https://www.dropbox.com/s/wyvnl9gtwokvc1c/HPP v1.0.0.zip?dl=0



Select "Direct download" to download the compressed HPP_v1.0.0.zip project. Extract the project to a folder onto your development PC.

The generated HPP bitstream and demo ELF files will be programmed through JTAG using the Xilinx SDK 2014.1 software. Open Xilinx SDK 2014.1.

sok Workspace Launcher		×
Select a workspace		
Xilinx SDK stores your projects in a folder called a workspace. Choose a workspace folder to use for this session.		
Workspace: I:\HPP_v1.0.0\HPP_v1.0.0\FreeRTOS\Workspace	~	Browse
Use this as the default and do not ask again		
	ОК	Cancel

Select the workspace directory of the extracted project located at <your_proj_dir>\<proj_name>\HPP_v1.0.0\FreeRTOS\Workspace.

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In the SDK Project Explorer on the left, right-click on "RTOSDemo" and "RTOSDemo_BSP" folders and select "Delete" from the menu.

SDK C/C++ - Xilinx	SDK		SDK Delete Resources	3 3		\times
File Edit Source	e Refactor Navigate ∋] ⊗ → ≪ →	Search Run Project	Are you sure you want to remove these 2 projects from	m the workspace	?	
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Click "OK" to confirm the deletion.

Warning: Do NOT select the "Delete project contents on disk (cannot be undone)" checkbox as this could permanently delete your project folders from a copied location.

Select menu option "File -> Import..." or right-click in Project Explorer and select "Import ...", then choose "Existing Projects into Workspace" under "General" and click "Next" to continue.

Click the "Browse" button to the Select root directory: <your_proj_dir>\<proj_name>\HPP_v1.0.0\FreeRTOS\FreeRTOS\Demo\CORTEX_A9_Zynq_ZC702\

Select "RTOSDemo" and "RTOSDemo_bsp" in Projects.

Warning: Do NOT select "Copy projects into workspace" since this will cause pathing dependency issues when rebuilding the project.



• F				
mport Projects Select a directory to sear	ch for existing	g Eclipse projects.		
Select root directory:	C:\XilinxPro	jects\HPP\HPP_S	fN\FreeRTOS\Free	Browse
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Import Projects

Click "Finish" to complete the import of the project into the active project workspace. Right-click on RTOSDemo in the Project Explorer and select "Clean Project" to clean and rebuild the project contents.



4 Running an Application on the HPP

The HPP uses a serial command-line interface (CLI) to interact with the operating system. You must have the required USB to UART Bridge Virtual COM Port (VCP) driver installed on the PC.

Download and install the latest Silicon Labs CP210x Windows VCP driver from https://www.silabs.com/products/development-tools/software/usb-to-uart-bridge-vcp-drivers

If you haven't already done so,

- Attach a USB cable from the development PC to Digital Lynx SX port "HPP Terminal"
- Attach a USB cable from the development PC to Digital Lynx SX port "HPP JTAG"

In Windows Device Manager, make a note of the COM port number "Silicon Labs CP210x USB to UART Bridge" connected to your HPP USB Terminal. The baud rate is 115200.



Windows device manager view of COM ports



To connect the PC for terminal access, a terminal program such as Tera Term or PuTTY is required.

If you don't already have a terminal program, you can download the latest version of Tera Term from https://ttssh2.osdn.jp/index.html.en

Or PuTTY from

https://www.chiark.greenend.org.uk/~sgtatham/putty/

The Xilinx SDK 2014.1 also provides a built-in terminal module. If the Terminal module window is not shown, select from the menu "Window -> Show View -> Terminal".

In the Terminal module window select the "Settings" icon (third icon from the left that looks like a note sheet <a>[].

/iew Title:	Terminal	
incoding:	ISO-8859-1	~
Connectior	n Type:	
Serial		~
Settings:		
Port:	COM15	~
Baud Rate:	115200	~
Data Bits:	8	~
Stop Bits:	1	~
Parity:	None	~
Flow Cont	rol: None	~
Timeout (s	ec): 5	

SDK Terminal Settings - serial port setup

In the Terminal Settings dialog, set "Connection Type" to "Serial" from the drop-down menu. In "Settings" choose your COM "Port" number connected to your HPP. Set the "Baud Rate" to 115200.

In the Terminal module window click the "Connect" icon (first icon from the left 👪) to connect to serial port.



In the SDK Project Explorer, right-click on RTOSDemo and select "Run As -> 1 Launch on Hardware (GDB)". This will create a default run configuration "RTOSDemo.elf" for this application.

In the SDK menu, select "Run -> Run configurations…" to edit the default run configuration. In the Target Setup tab, select the "Reset Entire System" option from the drop-down list and select the "Program FPGA" checkbox. In the Bitstream file box, click "Search…" and choose hpp_top.bit to select the bitstream file to download to the HPP FPGA. Click the "Apply" button to save and then "Run" to program the HPP over JTAG.



Run Configurations setup

This will begin a programming process visible in the lower right progress bar. This process usually takes approximately 15 seconds as it configures the FPGA, and downloads the bitstream and application.



Verify the following output is received over the serial connection in the Terminal window.



Type "help" and press Enter, to output a list of registered commands currently implemented in the HPP.



For more information or questions, please contact:

HPP_Support@neuralynx.com / 406-585-4542